



LABORATÓRIO DE INSTRUMENTAÇÃO
E FÍSICA EXPERIMENTAL DE PARTÍCULAS
partículas e tecnologia

MARTA DAQ V Connections

[**LIP** PARTICLES AND TECHNOLOGY]



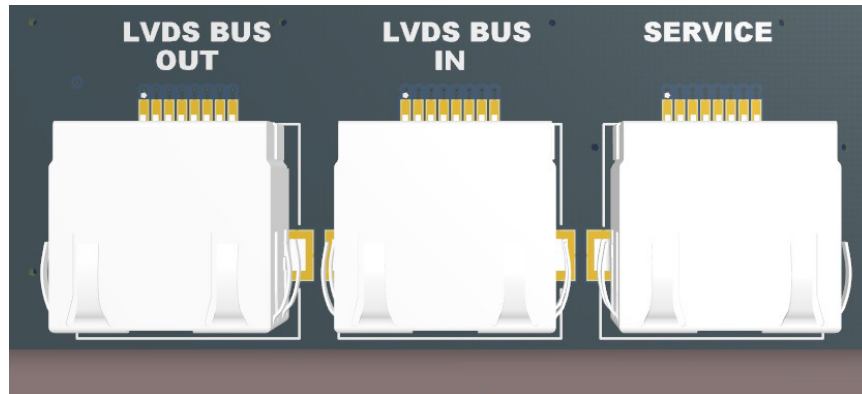
MARTA DAQ V

Connections

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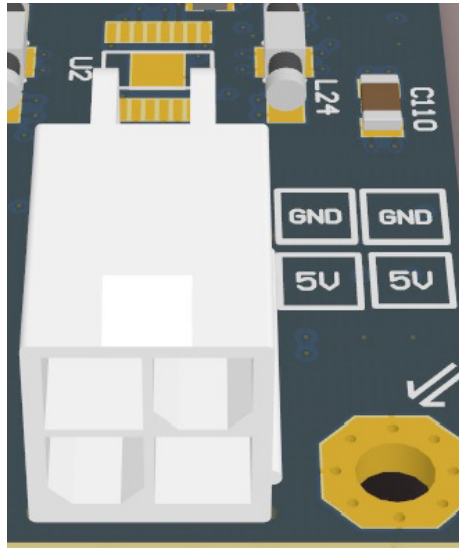
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Data Bus Connectors



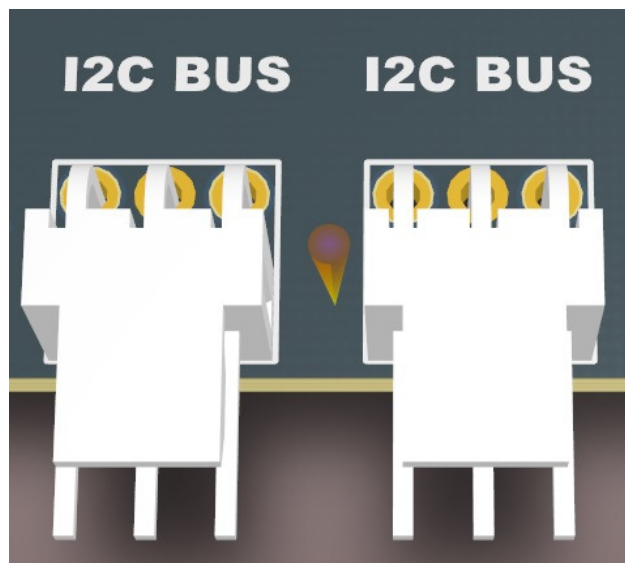
LVDS BUS OUT	LVDS BUS IN	SERVICE
PIN1 LVDS 1 N	PIN1 LVDS 1 N	PIN1 Ext_Trigger N
PIN2 LVDS 1 P	PIN2 LVDS 1 P	PIN2 Ext_Trigger P
PIN3 LVDS 2 N	PIN3 LVDS 2 N	PIN3 Ext_CLK N
PIN4 LVDS 2 P	PIN4 LVDS 2 P	PIN4 Ext_CLK P
PIN5 LVDS 3 N	PIN5 LVDS 3 N	PIN5 Trigger Out N
PIN6 LVDS 3 P	PIN6 LVDS 3 P	PIN6 Trigger Out P
PIN7 LVDS 4 N	PIN7 LVDS 4 N	PIN7 3V3 CMOS IO 1
PIN8 LVDS 4 P	PIN8 LVDS 4 P	PIN8 3V3 CMOS IO 2

Power Connector



Power Connector Pin out	
Top Left GND	Top Right GND
Bottom Left 5V	Bottom Right 5V

Slow Control Connectors



I2C BUS	
PIN Left SDA	PIN Left SDA
PIN Middle GND	PIN Middle GND
PIN Right SCL	PIN Right SCL

SMA Connectors



Trigger IN	Trigger OUT
Bottom Layer R61 and R63 Should be placed accordingly	Bottom Layer R60 and R62 Should be placed accordingly

C Test Connector



Input Stimulus for the ASIC

Negative pulse with maximum amplitude ΔV of 500mV (1pC)
